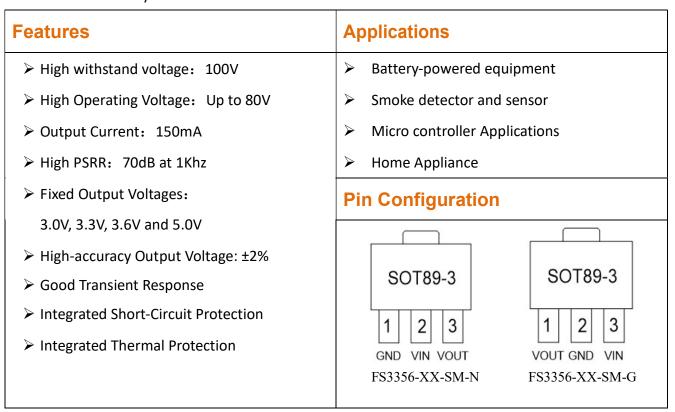


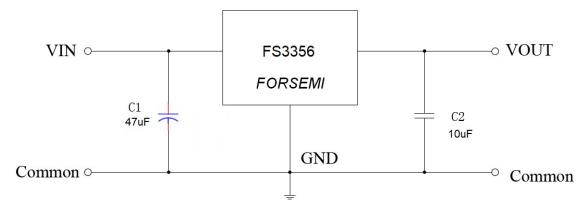
# **High Input Linear Voltage Regulator**

## **General Description**

The FS3356 series is a high input voltage, lowquiescent current, High PSRR linear regulator (LDO) able to provide 150mA load current. The LDO features very fast response against line voltage transient and load current transient, and ensures no overshoot voltage during the LDO start up and short circuit recovery.



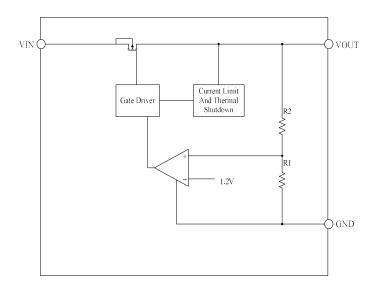
# **Typical Application Circuit**



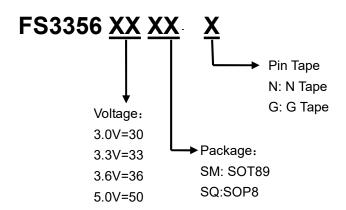
NOTE:, To ensure input stability, A minimum capacitor of C1=47µF Aluminum recommended For HOT PLUG application

FS –PRIMARY 2020 www.forsemi.com

## **Block Diagram**



## **Ordering Information**



## **Absolute Maximum Ratings**

Item	Description	Min	Max	Unit	
Voltage	VIN to GND	-0.3	100	V	
	VOUT to GND	-0.3	8	V	
Current	Peak output current	Internally limited			
Temperature	Operating Ambient Temperature	-40	85	Ç	
	junction Temperature	-	150	°C	
Thermal Resistance (Junction to Ambient)	SOT89	180 °C		°C/W	

**Note**. Stresses listed as the above "Maximum Ratings" may cause permanent damage to the device. Exposure to maximum rating conditions for extended periods may remain possibility to affect device reliability.

FS –PRIMARY 2020 www.forsemi.com



## **Electrical Characteristics**

#### (At $T_A=25^{\circ}$ C, $C_{IN}=47uF$ , $C_{OUT}=10uF$ , unless otherwise noted)

Symbol	Parameter	Test Conditions	MIN	TYP	MAX	UNIT
VIN	Operating Voltage		5		80	V
Ignd	Quiescent Current	Vin=12V, No load		3		uA
VOUT	Output Voltage	VIN=12V, IOUT=10mA	Vоитном * 0.98	Voutnom	Vоитном * 1.02	V
Iout_max	Output Current		_	150	_	mA
Vdrop	Dropout Voltage(4)	IOUT=10mA, VIN=VOUTNOM-0.1V	_	70	_	mV
		IOUT=100mA, VIN=VOUTNOM-0.1V	_	700	_	mV
ΔVουτ(Διουτ)	Load Regulation	VIN=12V, 1mA≤IouT≤100mA	_	0.02	_	%/mA
$\Delta V$ out( $\Delta V$ in)	Line Regulation	IOUT=1mA, Voutnom+0.5V≤VIN≤60V	_	0.01	_	%/V
Ilimit	Current Limit		_	250	_	mA
Tshdn	Thermal Shutdown Temperature	Shutdown, temperature increasing	_	150		°C
		Reset, temperature decreasing	_	140	_	
PSRR		Vin=12V , Iout=10mA F=1Khz,Vout=3.3V	_	70	_	dB

#### Note1.

Stresses listed as the above "Maximum Ratings" may cause permanent damage to the device.

#### Note2.

These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied.

#### Note3.

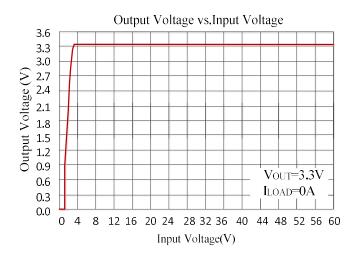
Exposure to maximum rating conditions for extended periods may remain possibility to affect device reliability.

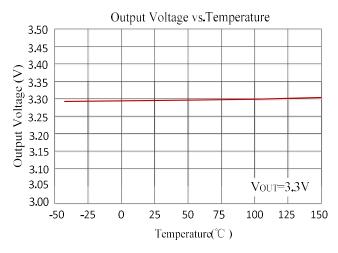
Dropout Voltage is the voltage difference between the input and the output at which the output voltage drops 2% below its nominal value.

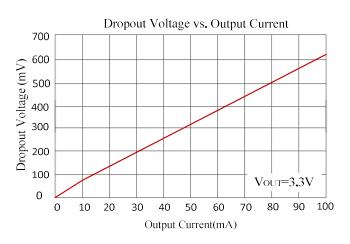
FS –PRIMARY 2020 www.forsemi.com

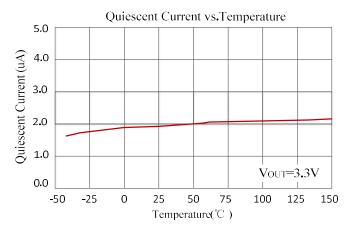


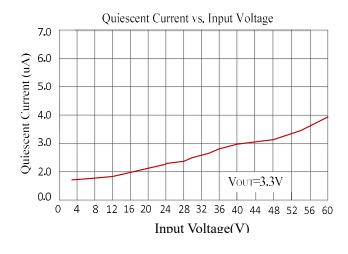
# **Typical Performance Characteristics**

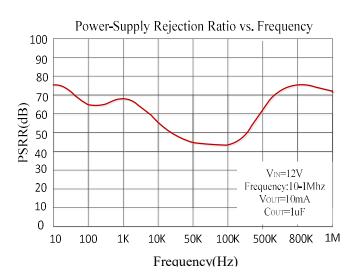








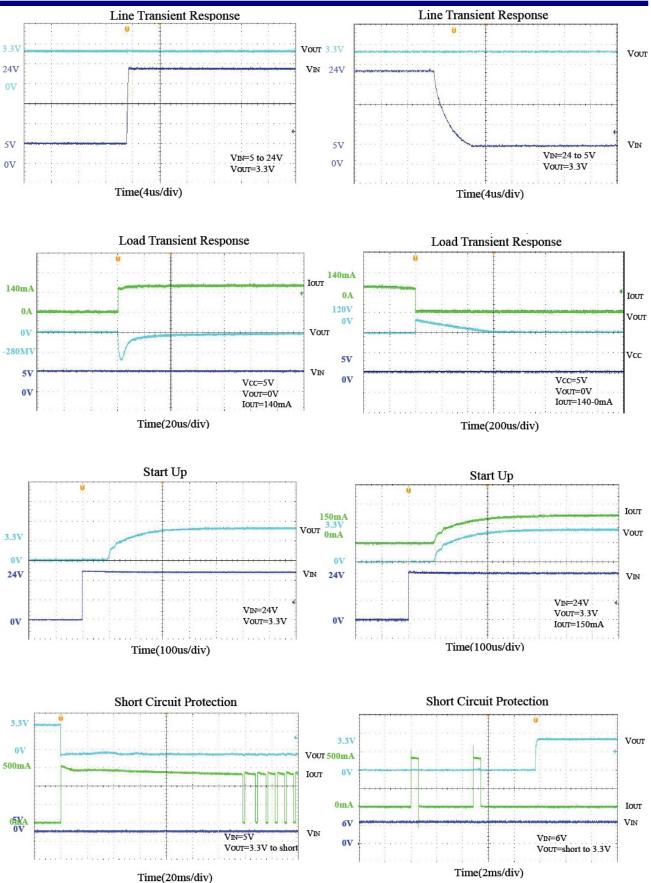




4/6



# **FS3356**



5/6



## **Functional Description**

#### **Input Capacitor**

A  $47\mu F$  Aluminum capacitor is recommended to connect between VIN and GND pins to decouple input power supply glitch and noise. The amount of the capacitance may be increased without limit. This input capacitor must be located as close as possible to the device to assure input stability and less noise. For PCB layout, a wide copper trace is required for both VIN and GND.

#### **Output Capacitor**

An output capacitor is required for the stability of the LDO. The recommended minimum output capacitance is  $1\mu$ F, ceramic capacitor is recommended, and temperature characteristics are X7R or X5R. Higher capacitance values help to improve load/line transient response. The output capacitance may be increased to keep low undershoot/overshoot. Place output capacitor as close as possible to VOUT and GND pins.

#### **Current Limit and Short Circuit Protection**

When output current at VOUT pin is higher than current limit threshold or the VOUT pin is direct short to GND, the current limit protection will be triggered and clamp the output current at a pre-designed level to prevent over-current and thermal damage.

#### **Thermal Protection**

The FS3356 has internal thermal sense and protection circuits. When excessive power dissipation happens on the device, such as short circuit at the output pin or very heavy load current with a large voltage drop across the device, the internal thermal protection circuit will be triggered, and it will shut down the power MOSFET to prevent the LDO from damage. As soon as excessive thermal condition is removed and the temperature of the device drops down, the thermal protection circuit will lease the control of the power MOSFET, and the LDO device goes to normal operation.

FS -PRIMARY 2020 www.forsemi.com